

Class	Subclass	Issue Classification

PATENT NUMBER

U.S. UTILITY Patent Application

DIPE	PATENT DATE
SEARCHED <i>Wm O. CR</i>	

APPLICATION NO. 09/819690	CONT/PRIOR F	CLASS 438	SUBCLASS 680	ART UNIT 2812	EXAMINER <i>D. Lee</i>
TITLE APPLICANTS → Satoshi Takano					
Semiconductor manufacturing method, substrate processing method, and semiconductor manufacturing apparatus					

ISSUING CLASSIFICATION							
ORIGINAL				CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS	CLASS	SUBCLASS	CLASS	SUBCLASS
INTERNATIONAL CLASSIFICATION							

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	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	(Primary Examiner) (Date)			ISSUE FEE Amount Due: _____ Date Paid: _____	
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